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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Initial Patent Application of:)
Serial No. 10/647,061)
Confirmation No. 9705)
Filing Date: AUGUST 22, 2003)
For: METHOD FOR MAKING SEMICONDUCTOR)
DEVICE INCLUDING BAND-)
ENGINEERED SUPERLATTICE)
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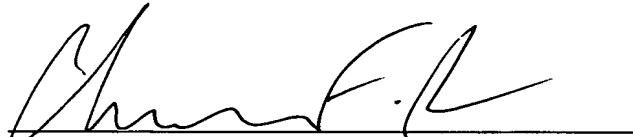
TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop Issue Fee
Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Enclosed are nine (9) sheets of formal drawings to be filed in the above-identified patent application.

Respectfully submitted,


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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: COMMISSIONER FOR PATENTS, ALEXANDRIA, VA 22313-1450, on this 29th day of April, 2004.

